



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Richard K. Williams
Assignee: Advanced Analogic Technologies, Inc.
Title: Modular Bipolar-CMOS-DMOS Analog Integrated Circuit And Power Transistor Technology
Serial No.: 10/767,751 Filing Date: 01/28/2004
Examiner: Matthew E. Warren Group Art Unit: 2815
Docket No.: AAT007-4C US Confirmation No: 5740

Santa Clara, California
August 18, 2006

MAIL STOP PETITION
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, §1.97 and §1.98, the Applicant submits for consideration in the above-identified patent application the documents listed on the accompanying Form PTO-1449. The Examiner is requested to make these documents of record.

This Supplemental Information Disclosure Statement is submitted in conjunction with the accompanying Request for Continued Examination.

Applicant would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

The submittal of this Supplemental Information Disclosure Statement is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the

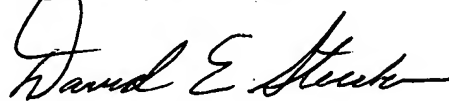
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information, protocols, results and the like reported by third parties are accurate or enabling;
or (iv) the above information constitutes prior art to the subject invention.

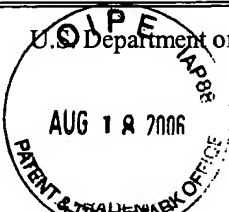
Respectfully submitted,

A handwritten signature in black ink, appearing to read "David E. Steuber". The signature is fluid and cursive, with a large initial "D" and "S".

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 <p>U.S. Department of Commerce, Patent and Trademark Office</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(Use several sheets if necessary)</p>	Application No.:	10/767,751
	Filing Date:	01/28/2004
	First Named Inventor:	Richard K. Williams
	Group Art Unit:	2815
	Examiner Name:	Matthew E. Warren
	Confirmation No.:	5740
	Attorney Docket No.:	AAT007-4C US

U.S. Patent Documents							
*Examiner Initials		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1.	5,789,788	08-1998	Erma et al.	257	371	
	2.	6,730,964	05-2004	Horiuchi	257	347	
	3.	6,031,267	02-2000	Lien	257	344	
	4.	2002/0135023	09-2002	Madhukar et al.	257	371	
	5.	2003/0052371	03-2003	Matsuzaki et al.	257	371	
	6.	6,777,758	08-2004	Yamashita et al.	257	369	

Foreign Patent Documents							Translation	
		Document	Date	Country	Class	Subclass	Yes	No

Non Patent Literature Documents		

Examiner:	Date Considered:
<p>* Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication with applicant.</p>	

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